power supply or be connected

VDD_SOC_IN

VDDSOC_IN_1

VDDSOC_IN_2

VDDSOC_IN_4

VDDSOC_CAP_1

VDDSOC_CAP_2

VDDSOC_CAP_4

VDDSOC_CAP_7

VDDSOC_CAP_11

VDDSOC_CAP_14

VDDARM_CAP_1

VDDARM_CAP_5

VDDARM_CAP_6

NVCC_3V0

NVCC_1V8

CCD-100K/X5R/25

CCD-100K/X5R/25

CCD-100K/X5R/25

CC-10K/25V

CC-10K/25V

CC-22uF/10V

VDD_HIGH_CAP

VDD_SOC_CAP
# i.MX6Q - DRAM

## Diagram Description:

- **DRAM Clock Terminators:** These terminate the clock signals to the DRAM chip to ensure proper clock distribution.
- **DRAM Chip:** The diagram shows the connections to the DRAM chip, including address, data, and control lines.
- **Power Supplies:** Various power supplies are connected to different components, ensuring stable operation conditions.

### Key Components:

- **DDR_1_5V:** This voltage is used for the DDR memory operation.
- **VDD3, VSS3:** These supply voltages are used for certain power rails.

### Connections:

- **Address Lines:** A1 to A15, A10/BC#.
- **Data Lines:** DQ0 to DQ15.
- **Control Lines:** WE#, CS#, CKE, CK#, CAS#.

### Power Supplies:

- **NVCC_DRAM_2P5, NVCC_DRAM_9:** These are the voltage supplies specifically for the DRAM to ensure proper operation.

### Diagram Notes:

- The diagram includes various resistors (R-10K, R-240 1%) and capacitors (CCD-100K/X5R/25) for proper signal integrity and noise reduction.
- The layout is detailed to show the physical connections and placements of components.
or equal to 4 seconds the part will turn on the part and if the sensitive input, an active high signal turns on the part and an active low turns off the part, or a mechanical switch, a momentary low puts it into SLEEP mode. As a time sensitive input.

The SW2 pin can be configured as output a low, shutdown control, or as an active high level sensitive input. On DC mode, the SW2 pin can be configured as output a low, shutdown control, or as an active high level sensitive input. The SW2 pin can be configured as output a low, shutdown control, or as an active high level sensitive input. The SW2 pin can be configured as output a low, shutdown control, or as an active high level sensitive input.